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*****
2380 Thu Feb 12 20:33:23 2015
new/usr/src/uts/armv6/loader/fakeloader_core.s
loader: simplify MMU enabling code
*****
_____unchanged_portion_omitted_____
87 #endif /* __lint */
89 #if defined(__lint)
91 /* ARGSUSED */
92 void
93 fakeload_mmu_enable(void)
94 {}
96 #else /* __lint */
98 /*
99  * We first make sure that the ARMv6 pages are enabled (bit 23) and then
100 * enable the MMU (bit 0).
101 */
102 ENTRY(fakeload_mmu_enable)
103 mrc    p15, 0, r0, c1, c0, 0
104 orr    r0, #0x800000      /* enable ARMv6 pages */
105 orr    r0, #0x1           /* enable MMU */
104 orr    r0, #0x800000
105 mcr    p15, 0, r0, c1, c0, 0
106 mrc    p15, 0, r0, c1, c0, 0
107 orr    r0, #0x1
106 mcr    p15, 0, r0, c1, c0, 0
107 bx    lr
108 SET_SIZE(fakeload_mmu_enable)
_____unchanged_portion_omitted_____
```